



# 4th RD51 Collaboration Meeting

## Monday, 23 November 2009

### WG5: MPGD Related Electronics - 6/2-004 (14:00 - 19:00)

time	[id] title	presenter
14:00	[31] Welcome, Announcements, EVO	MULLER, Hans Dr KAMINSKI, Jochen
14:05	[32] From Chip matrix to 'ideal chip' for MPGDs	Dr MARTOIU, Sorin
14:25	[33] MSGCROC - An ASIC for high count rate readout of position sensitive gas chambers	Dr MINDUR, Bartosz
14:50	[43] VFAT : A 128 channel chip for charge sensitive readout of multi-channel silicon & gas particle detectors	ASPELL, Paul
15:10	[34] Ethernet-driven control and data acquisition scheme for the Timepix-based TPC readout	SCHAEFER, Uli
15:30	break	
15:50	[35] News from the Scalable Readout System	MULLER, Hans
16:05	[37] Analogue chip adapter for SRS	MARTOIU, Sorin
16:25	[38] Porting the DATE detector readout to GbE	COSTA, Filippo
16:40	[41] DATE status for the NEXT experiment	Mr DIAZ, Luis
16:50	[39] Status VME-based readout system for Jlab GEM tracker	CISBANI, Evaristo
17:10	[40] News on DTC link for SRS	WANG, Dong
17:25	[42] ESD diode integration on MM chamber frontend	MULLER, Hans