Announcements

Working Group-5 on Common Electronics

WG5 Tasks:

Definition of front end electronics requirements for MPGDs
Development of general purpose pixel chip for active anode readout
Development of large area detectors with pixel readout
Development of portable multichannel data acquisition systems for detector studies
Discharge protection strategies

Convenors: Hans Muller CERN PH, Jochen Kaminski, Bonn University

WEB page of WG5

https://espace.cern.ch/rd51-wg5/default.aspx

Administrator: Sorin Martoiu

So far active: Chip matrix and Scalable RO system
Please contribute on more activities

Email WG5
rd51-wg5-contacts@cern.ch

Please register, either via the CERN IT page
-> MMM services, Email groups
or by sending email request to the convenors

HAVE a nice meeting !